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Claims 1 and 8 were rejected as unpatentable over Beauducel in view of Palmer. Claims 2-4 and 11 were rejected as unpatentable over Beauducel in view of Palmer in view of Potratz. Claim 5 was rejected as unpatentable over Beauducel in view of Palmer in view of Scott. Claims 6-7 were rejected as unpatentable over Beauducel in view of AAPA. Claims 12 and 13 were rejected for purported lack of disclosure. Applicant requests reconsideration.

The claims were amended to more clearly state the communication medium. At the point of novelty, the examination states Beauducel teaches at Col 3 lines 59-62 that claim limitation of "the modulated binary laser signal having a pulse width having a duration representative of the analog input signal (inherent in modulation)". Well then, if the pulse width is inherently modulated, then pulse width is no longer a pulse width, but an inherently modulated one. The examiner is confusing the analog value modulating the pulse width as the analog value changes, (that is, the pulse width horizontal duration changes as the analog value input changes), with the inherent vertical modulation of the binary signal, that is modulated, to provide both the synchronization and the non-binary (tri-state) coding, as specifically described at Col 3 lines 45-62. If the examiner fails to understand this distinction in modulation, then perhaps the examiner can explain what effect the synchronization element 5 that drives the sigma delta modulator, and the coding circuit 6 that is in turn driven by the sigma delta modulator, have on the signal that is actually

transmitted. Applicant kindly requests that the examiner fully understand the import of the words "applying a predetermined coding allowing a clock signal to be conveyed at the same time as the signals, such as the HDB3 code, a multi-level code, the CMI-3 code suited to an optical type transmission, etc." Here, Beauducel is teaching that the transmitted signal is encoded with a clock prior to transmission, and is not simply an on and off binary signal having a pulse width that is asynchronously communicated. The receiver must further have a decoding circuit as taught at Col 3 lines 63 to Col line 6. Perhaps the examiner is confused by the difference between modulating the analog input signal into a binary signal having a pulse width that is modulated in horizontal length so as to represent the analog value by virtue of that horizontal length, versus, modulating the signal as to its height so as to encode and embed synchronous clocking signals and transitions for synchronous communications. These two types of modulations are different yet both characterized as "modulations", and this may be confusing, to one, very unskilled in the art.

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To resolve any doubt, the examiner is kindly directed to read claim 1 of Beauducel wherein it states, "transmitting for each local modulator to the remote station a clock signal allowing a synchronization of the remote station with each local modulator". Continuing, in claim 2, it states "transmitting the clock signal with each bit stream by coding each bit stream with the clock signal". Beauducel teaches and claims encoding the communicated signal with a clock for synchronized communications, the very problem that the present invention solves.

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Palmer teaches using sigma delta modulators for use in a laser communication system for transmitting synchronized digital signals. Beauducel teaches generating synchronized and non-binary (coded) signals. The cited references do not suggest a solution to transmitting an analog value, that is, information content, asynchronously using in a laser binary signal having a pulse width representing that value. Allowance of the claims is requested.

Respectfully Submitted

Gerrick Michael Reid

Derrick Michael Reid



## CERTIFICATE OF MAILING

I, hereby certify that this correspondence is being deposited in the United States Postal Service in an envelope with First Class full postal prepaid thereon addressed to: Mail Stop Amendments Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: 08/07,2006

Derrick Michael Reid

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